

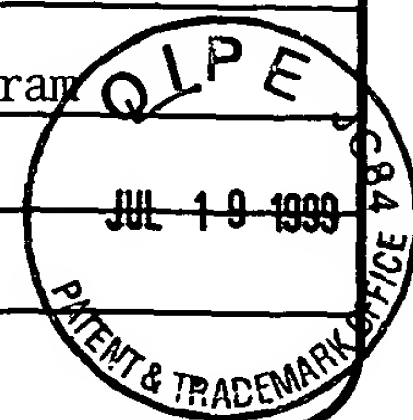
GP 2812

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PTO/SB/21 (6-98)  
Approved for use through 09/30/2000. OMB 0651-0031  
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	09/292/132 ✓
	Filing Date	April 14, 1999 ✓
	First Named Inventor	Salman Akram
	Group Art Unit	2812
	Examiner Name	Unknown
Total Number of Pages in This Submission	3	Attorney Docket Number MI22-1171



ENCLOSURES (check all that apply)		
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<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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<input type="checkbox"/> After Final	<input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition	<input type="checkbox"/> Proprietary Information
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<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	No fees required.	

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Firm or Individual name	James D. Shaurette Reg. No. 39,833 Wells, St. John, Roberts, Gregory & Matkin P.S. 601 W. First Ave., Suite 1300, Spokane, WA 99201-3828
Signature	
Date	July 14, 1999

CERTIFICATE OF MAILING	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. . . . . 09/292,132  
Filing Date . . . . . April 14, 1999  
Inventor . . . . . Salman Akram et al.  
Assignee . . . . . Micron Technology, Inc.  
Group Art Unit . . . . . 2812  
Examiner . . . . . Unknown  
Attorney's Docket No. . . . . MI22-1171  
Title: Methods of Forming a Transistor Gate (As Amended)



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT


References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted

Dated: July 14, 1999

Attorney:

  
James D. Shaurette  
Reg. No.: 39,833

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